

Docket No.: 60188-075

PATENT

IN THE UNITED STATES PATENT AND TRADEMERK OFFICE

In re Application of

Makoto FUJIWARA

Serial No.:

Group Art Unit:

Filed: May 31, 2001

Examiner:

For:

SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING

SEMICONDUCTOR INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits.

No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: May 31, 2001

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SHEET 1 OF 1

. INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 60188-075		SERIAL NO.		
				APPLICANT Makoto FUJIWARA				198/
				FILING DATE May 31, 2001		GROUP	1100	
33.4		Į.	S. PATENT	DOCUMENT	S			
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME		CLASS	SUBCLASS	FILI	NG DATE
	4,905,142	FEB. 27, 1990	MATSUBARA, ET AL.					
	6,006,354	DEC. 21, 1999	MCGUINNESS					
		FOR	EIGN PATE	NT DOCUME	NTS			
EXAMINER'S INITIALS	PATENT NO.	DATE	СО	COUNTRY		SUBCLASS	Translation Yes No	
							165	No
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	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER				DATE CONSIDERED				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.